

Design & Verification Training Schedule

Live Online Training for Europe & Asia

Digital Design

Essential Digital Design Techniques November 24th, 2025

VHDL & Verilog

Expert VHDL	October 20th, 2025
Comprehensive VHDL	November 4th, 2025
Comprehensive Verilog	November 17th, 2025

SystemVerilog & UVM

Comprehensive SystemVerilog	October 8th, 2025
UVM Adopter Class	October 27th, 2025
SystemVerilog for New Designers	November 3rd, 2025

Formal Verification

Essential Formal Verification	November 3rd, 2025
Advanced Formal Verification	December 1st, 2025

In-Person Training for UK

Expert VHDL	October 13, 2025	Hampshire, UK
Community and the MIDI	5-h	Hananahina III
Comprehensive VHDL	February 9th, 2026	Hampshire, UK
Comprehensive SystemVerilog	March 2nd, 2026	Hampshire, UK
UVM Adopter Class	March 30th, 2026	Hampshire, UK

BOOK TODAY: www.doulos.com/schedule

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